| | Application No. | Applicant(s) | |
|--|--|--------------|----|
| Notice of Allowability | 10/660,746 | OTAKE ET AL. | |
| | Examiner | Art Unit | / |
| | Richard Hanig | 2873 | p. |
| The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. | | | |
| 1. This communication is responsive to | | | |
| 2. The allowed claim(s) is/are <u>1-23</u> . | | | |
| 3. The drawings filed on 12 September 2003 are accepted by the Examiner. | | | |
| 4. | | | |
| Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 09/12/03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 6. ☐ Interview Summa Paper No./Mail D 8), 7. ☐ Examiner's Amen | Date | |

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DETAILED ACTION

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1) The following is an examiner's statement of reasons for allowance: The reference Morl et al show a segmented traveling wave electrode in fig. 1, and in fig.2 there is shown a semiconductor under the electrode, but there is no showing that this semiconductor is segmented with electrical separators as is claimed by the instant invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

- 2) The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Walker (US 2003/0190107) in [0022] discusses using a semiconductor next to the waveguide to confine the electrical field.
- 3) Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard Hanig whose telephone number is 571-272-2329. The examiner can normally be reached on M-F: 8:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Georgia Epps can be reached on 571-272-2328. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Scott J. Sugarman Primary Examiner

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